

# PART B - FEE(S) TRANSMITTAL

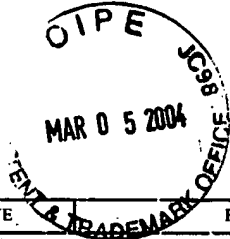
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24998 7590 05/20/2003

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(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/941,761	08/30/2001	Phillip E. Byrd	M4065.0468/P468	7373
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TITLE OF INVENTION: METHOD AND STRUCTURE FOR TEMPORARILY ISOLATING A DIE FROM A COMMON CONDUCTOR TO FACILITATE WAFER LEVEL TESTING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1300	\$300	\$1600	08/20/2003
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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STEVENSON, ANDRE C	2812	438-347000
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## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Micron Technology, Inc.

Boise, ID

Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Advance Order - # of Copies 5

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(Authorized Signature)

(Date)

Thomas J. D'Amico Reg. No. 28,371 8/20/03

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